

DUAL MODE ANALOG DIFFERENTIAL AND CMOS LOGIC CIRCUIT

Abstract of the Disclosure

5 A dual mode, analog differential and complementary metal oxide semiconductor (CMOS) logic circuit is provided. The circuit includes a differential input for receiving a differential input signal. A switch pair is coupled to the differential input. A pair of load resistors coupled to the switch pair defines a differential output for providing a differential output signal. A current source is coupled to the switch pair. A control input receives a control signal and control circuitry coupled to the control input
10 disable the current source to select a CMOS testing mode responsive to the control signal being activated.